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APPLICATION NO. FILING DATE		1	FIRST NAMED INVENTOR		ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/751,521			Hiroshi Takenaka		60188-656	3283	
TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT			DEVICE AND EXPOSURE METHOD				
					TMC DEUTCE DECT	ONG AND	
		CONDUCTOR 1	NTEGRATED	CIRCUIT INCLUD	ING DEVICE REGIO	DNS AND	
APPLN, TYPE	ING REGION SMALL ENTITY	ISSUE FI	EE .	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE	
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NGUYEN, HA T		2812		438-599000			
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MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD. OSAKA, JAPAN							
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